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DATE: January 3, 2005

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**To:**

NAME	FAX No.	PHONE No.
Examiner, Thuan V. Do USPTO	(703) 872-9306	

FROM: Rimma Budnitskaya  
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NUMBER OF PAGES WITH COVER PAGE: 2

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**MESSAGE:**

Application No.: 10/029,521  
Filing Date: December 21, 2001

*Please See The Attached*

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IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Christopher F. Bevis  
SERIAL NO.: 10/029,521  
FILING DATE: December 21, 2001  
TITLE: Inspection of Semiconductor Devices Using Design Data  
EXAMINER: Thuan V. Do  
GROUP ART UNIT: 2825  
ATTY. DKT. NO.: 22120-06499

**CERTIFICATE OF FACSIMILE TRANSMISSION**

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Dated: January 3, 2005

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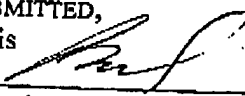
Rimma Budnitskaya, Reg. No. 48,237

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**INTERVIEW SUMMARY**

Applicant's representative Rimma Budnitskaya, Reg. No. 48,237, and Examiners Thuan V. Do conducted a telephonic interview on December 6, 2004 regarding the above-referenced application. Claim 1 and Figures 1 and 5 and prior art of record Nara were discussed. Examiner Do agreed to review the prior art relative to claim 1 and review the arguments submitted by the Applicant on September 23, 2004.

Date: January 3, 2005

RESPECTFULLY SUBMITTED,  
Christopher F. Bevis  
By:   
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